

IN THE UNITED STATES PATENT OFFICE

Applicant: Alireza S. Kaviani
Assignee: Xilinx, Inc.
Title: "HARD PHASE ALIGNMENT OF CLOCK SIGNALS WITH AN
OSCILLATOR CONTROLLER"

Serial No.: Not Yet Known File Date: 3/17/2004
Examiner: Not Yet Known Art Unit: Not Yet Known

Continuation In Part of
Serial No.: 10/300,446 File Date: 11-19-2002
Docket No.: X-1203-1P US

COMMISSIONER FOR PATENTS
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INFORMATION DISCLOSURE STATEMENT

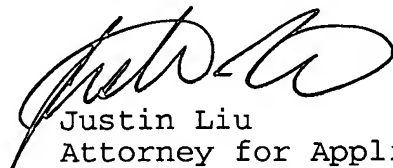
Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicant brings to the attention of the Examiner the ten (10) references listed in the attached Substitute for Form PTO-1449 (Information Disclosure Statement by Applicant).

All of these references were cited in prior related U.S. patent application Serial Number 10/300,446 filed November 19, 2002 to which this application claims priority. Copies of these references have not been supplied herein since they were previously submitted in the parent case.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully Submitted,



Justin Liu
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